



### D4KB05 thru D4KB10

### D3K

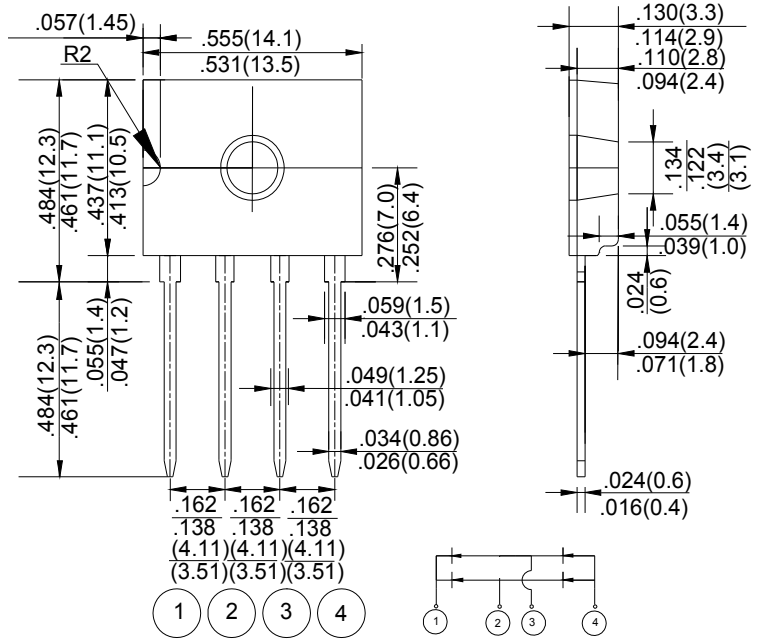
- 50 to 1000Volts
- 4.0 Amperes

#### REVERSE VOLTAGE FORWARD CURRENT FEATURES

- Glass passivated chip junction
- High case dielectric strength
- High surge current capability
- Ideal for printed circuit board

#### MACHANICAL DATA

- Terminal:Plated leads solderable per MIL-STD 202E, Method 208C
- Case:UL-94 Class V-0 recognized Flame Retardant Epoxy
- Polarity:Polarity symbol marked on body
- Mounting position:any



Dimensions in inches and (millimeters)

### MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

Rating at 25°C ambient temperature unless otherwise specified.

Single phase, half wave ,60Hz, resistive or inductive load.

For capacitive load, derate current by 20%

CHARACTERISTICS	SYMBOL	D4KB05	D4KB1	D4KB2	D4KB4	D4KB6	D4KB8	D4KB10	UNIT
Maximum Recurrent Peak Reverse Voltage	$V_{RRM}$	50	100	200	400	600	800	1000	V
Maximum RMS Voltage	$V_{RMS}$	35	70	140	280	420	560	700	V
Maximum DC Blocking Voltage	$V_{DC}$	50	100	200	400	600	800	1000	V
Maximum Average Forward Rectified Output Current @ $T_c=138^\circ\text{C}$ (with heatsink)	$I_{(AV)}$	4							A
Peak Forward Surge Current 8.3ms Single Half Sine-Wave Super Imposed on Rated Load (JEDEC Method)	$I_{FSM}$	135							A
Maximum Forward Voltage at 2.0A DC	$V_F$	1.0							V
Maximum Forward Voltage at 4.0A DC	$V_F$	1.1							V
$I^2t$ Rating for Fusing ( $t<8.3\text{ms}$ )	$I^2t$	75.63							$\text{A}^2\text{s}$
Typical Thermal Resistance	without heatsink	$R_{\theta Ja}$							$^\circ\text{C/W}$
	with heatsink	$R_{\theta Jc}$							
	without heatsink	$R_{\theta JL}$							
Maximum DC Reverse Current at Rated DC Blocking Voltage	@ $T_a=25^\circ\text{C}$	10.0							$\mu\text{A}$
	@ $T_a=125^\circ\text{C}$	500							
Operating Temperature Range	$T_J$	-55 to +150							$^\circ\text{C}$
Storage Temperature Range	$T_{STG}$	-55 to +150							$^\circ\text{C}$

NOTES:The typical data above is for reference only(典型值仅供参考).



FIG.1-DERATING CURVE OUTPUT RECTIFIED CURRENT

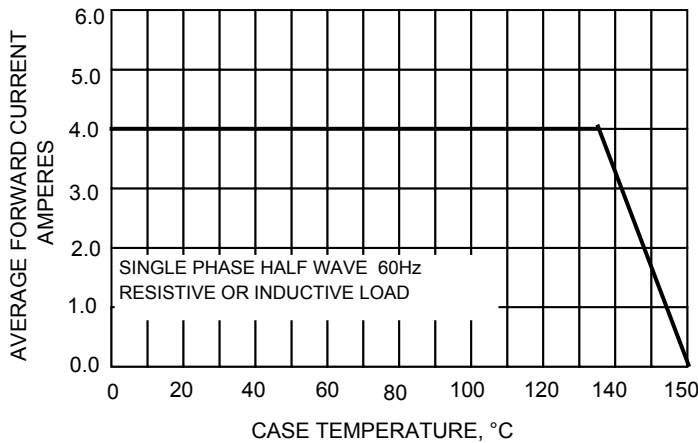


FIG.2-MAXIMUM NON-REPETITIVE SURGE CURRENT

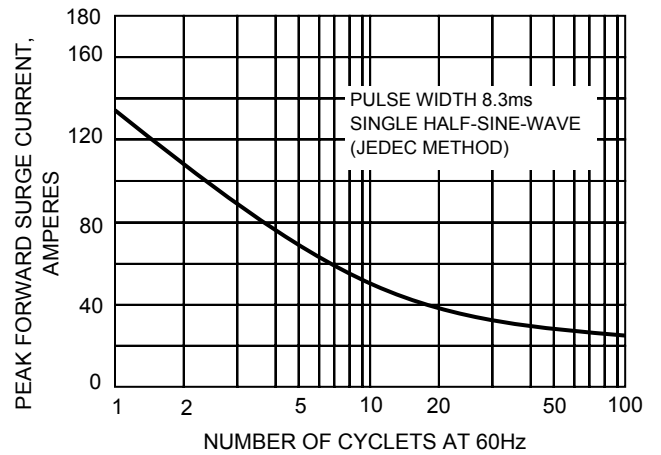


FIG.3-TYPICAL JUNCTION CAPACITANCE

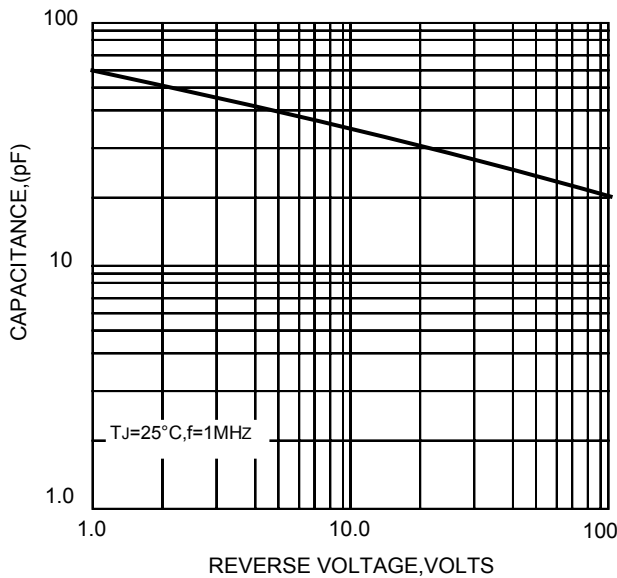


FIG.4-TYPICAL FORWARD CHARACTERISTICS

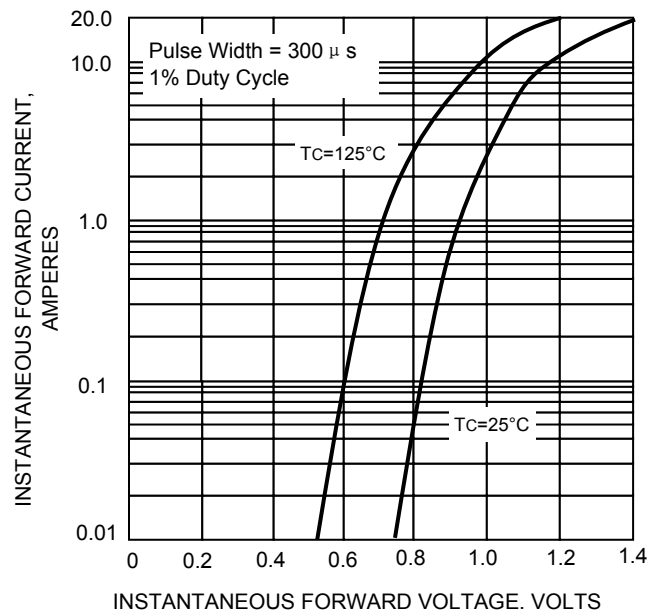


FIG.5-TYPICAL REVERSE CHARACTERISTICS

